

C.U.SHAH UNIVERSITY

Summer Examination-2017

Subject Name : Advanced Micro-Processors

Subject Code : 4TE05AMP1

Branch: B.Tech (CE)

Semester : 5

Date : 30/03/2017

Time : 02:30 To 05:30

Marks : 70

Instructions:

- (1) Use of Programmable calculator & any other electronic instrument is prohibited.
- (2) Instructions written on main answer book are strictly to be obeyed.
- (3) Draw neat diagrams and figures (if necessary) at right places.
- (4) Assume suitable data if needed.

Q-1

Attempt the following questions:

- a) Define : Assembler. (01)
- b) Define : Embedded system. (01)
- c) Define : Micro-controller. (01)
- d) True/False: 4004 is the first microprocessor built by the Intel Corporation. (01)
- e) True/False: 8085 has 6 byte instruction queue. (01)
- f) The OF is called as _____. (01)
A. overflow flag B. overdue flag C. one flag D. over flag
- g) STA 2005 instruction is how many byte of instruction? (01)
A) 4
B) 2
C) 1
D) 3
- h) Intel 8086 microprocessor is a _____ processor. (01)
A) 4 bit B) 32 bit
C) 8 bit D) 16 bit
- i) The CF is known as _____. (01)
A) carry flag B) condition flag C) common flag D) single flag
- j) The _____ contains an offset instead of actual address (01)
A) SP B) IP C) ES D) SS
- k) The _____ translates a byte from one code to another code. (01)
A. XLAT B. XCHNG C. POP D. PUSH
- l) The 16 bit flag of 8086 microprocessor is responsible to indicate _____. (01)
A. the condition of result of ALU operation B. the condition of memory
C. the result of addition D. the result of subtraction
- m) Which group of instructions does not affect the flags? (01)
A. Arithmetic operations B. Logic operations
C. Data transfer operations D. Branch operations
- n) INC destination increments the content of destination by _____. (01)
A. 1 B. 2 C. 30 D. 41



